

# Abstracts

## Effects of dummy patterns and substrate on spiral inductors for sub-micron RF Ics (2002 [RFIC])

---

*Jae-Hong Chang, Yong-Sik Youn, Hyun-Kyu Yu and Choong-Ki Kim. "Effects of dummy patterns and substrate on spiral inductors for sub-micron RF Ics (2002 [RFIC])." 2002 Radio Frequency Integrated Circuits (RFIC) Symposium 02. (2002 [RFIC]): 419-422.*

In today's sub-micron CMOS technologies, dummy patterns are necessary to obtain the desired metal density for uniform etching. This paper shows the effect of the dummy patterns on the quality factor of the inductor. The effects of the polysilicon ground shield and p-doped substrate on inductor performance have also been investigated. As the distance of between dummy and inductor is increased, the quality factor is less influenced by eddy current loss due to the dummy. Also we can achieve  $Q = 13$  at 3 GHz and  $L = 6.05 \text{ nH}$  using a patterned ground shield with slotted polysilicon layers in a commercial standard 0.18  $\mu\text{m}$  CMOS technology.

[Return to main document.](#)